Live Demonstration: Real-time motor rotation frequency detection by spike-based visual and auditory AER sensory integration for FPGA

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Abstract— Multisensory integration is commonly used in various robotic areas to collect much more information from an environment using different and complementary types of sensors. This demonstration presents a scenario where the motor rotation frequency is obtained using an AER DVS128 retina chip (Dynamic Vision Sensor) and a frequency decomposer auditory system on a FPGA that mimics a biological cochlea. Both of them are spike-based sensors with Address-Event-Representation (AER) outputs. A new AER monitor hardware interface, based on a Spartan-6 FPGA, allows two operational modes: real-time (up to 5 Mevps through USB2.0) and off-line mode (up to 20Mevps and 33.5Mev stored in DDR RAM). The sensory integration allows the bio-inspired cochlea limit to provide a concrete range of rpm approaches, which are obtained by the silicon retina.

I. INTRODUCTION

This demonstration consists of the system shown in the photo (Fig. 1)[1]. A circle of metal with a shape drawn is mounted on a DC motor that will be rotating while DVS and bio-inspired cochlea are capturing information from the scene. The two spike-based sensors information are transmitted on board where both are merged and captured. Two operation modes are implemented in this controller board: real-time, in which all information received from the sensor is time stamped and sent to the PC through USB 2.0 board interface; and off-line, where sensor information is stored in DDR2 RAM memory, together with precise temporal information, allowing later processing in a PC by downloading it from DDR2.

II. HARDWARE INVOLVED

The components used in this demonstration are: a DC motor with a metal circle; a DVS128 silicon retina; a bioinspired auditory system (microphone, equalizer and Virtex5 development board); and the XEM 6010 Opal Kelly board.

The event merger and monitor/logger circuit captures this information. It is implemented on the Opal Kelly Spartan 6 FPGA. As discussed above, monitor/logger circuit allows two operations mode: real-time and off-line.

In real-time mode, the AER data stream from the both sensor is time stamped and sent to a PC directly through a USB 2.0 interface. The maximum amount of information captured is limit up 5 Mevps. This is due to the bottleneck of the USB 2.0.

In off-line mode the maximum amount of information captured by the system is higher (about 20 Mevps) because the events are time-stamped and stored immediately in DDR2 memory where the FPGA-DDR2 interface is faster than USB 2.0 interface.

For both methods the monitor/logger circuit sends the information to the PC where it is processed under jAER software application. In this software application, the AER DVS128 sensor information is used by a filter to estimate the motor speed while the frequency decomposer auditory system information is processed by a Convolutional Neural Network (CNN) to get a valid range of motor speed. The output of AER DVS128 filter that be out of this valid range, will be rejected in order to calculate de final system output.



Fig. 1. Completed setup with DC motor, bio-inspired cochlea system (up rigth corner), monitor/logger system (down right cornet) and DVS128 silicon retina (middle).

III. VISITORS EXPERIENCE

Visitors can interact with the system by changing, through a microcontroller, the motor rotation frequency while sensors are capturing events to estimate the motor frequency.

Reference

[1] A. Rios-Navarro, A. Jimenez-Fernandez, E. Cerezuela-Escudero, M. Rivas, G. Jimenez-Moreno, A. Linares-Barranco. "Live Demonstration: Real-Time Motor Rotation Frequency Detection by Spike-Based Visual and Auditory Sensory Fusion on AER and FPGA" LNCS 8681, pp 847-848 ISSN 0302-9743 ISBN 978-3-319-11178-0

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