

Design Rule Verification Report

Date : 11/09/2007
 Time : 16:24:16
 Elapsed Time : 00:00:01
 Filename : [C:\Archivos de programa\Altium Designer 6\Examples\PRUEBA\cc2431em.PcbDoc](#)

Summary

Warnings	Count
Total	0

Rule Violations	Count
Width Constraint (Min=0.254mm) (Max=1mm) (Preferred=0.4mm) (InNet('Net*_2'))	0
Width Constraint (Min=0.254mm) (Max=1mm) (Preferred=0.4mm) (InNet('NetC341_1'))	0
Hole Size Constraint (Min=0.025mm) (Max=2.54mm) (All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Prefered=12.7mm) (All)	0
Width Constraint (Min=0.254mm) (Max=0.254mm) (Preferred=0.254mm) (All)	0
Clearance Constraint (Gap=0.2mm) (InNet('GND')), (All)	0
Broken-Net Constraint ((All))	0
Short-Circuit Constraint (Allowed=No) (All), (All)	0
Width Constraint (Min=0.254mm) (Max=1mm) (Preferred=0.4mm) (InNet('AVDD 1,8V'))	0
Width Constraint (Min=0.254mm) (Max=1mm) (Preferred=0.4mm) (InNet('3,3V'))	0
Clearance Constraint (Gap=0.3mm) (InNet('GND') and InPolygon), (All)	0

Width Constraint (Min=0.254mm) (Max=0.4mm) (Preferred=0.4mm) (InNet('NetCC2431_33'))	0
Total	0
