

0.13- μm CMOS Tunable Transconductor Based on the Body-Driven Gain Boosting Technique with Application in Gm-C Filters

T. Sánchez-Rodríguez and R.G. Carvajal

Departamento de Ingeniería Electrónica
Escuela Superior de Ingenieros, Universidad de Sevilla,
SPAIN
{trinidad.sanchez, carvajal}@gte.esi.us.es

Salvatore Pennisi

DIEES-Dipartimento di Ingegneria Elettrica
Elettronica e dei Sistemi
University of Catania, ITALY

J. Galán

Dpto. de Ingeniería Electrónica, de Sistemas
Informáticos y Automática
University of Huelva, SPAIN

Abstract— We present a low-voltage low-power CMOS tunable transconductor exploiting body gain boosting to increase the small-signal output resistance. As a distinctive feature, the proposed scheme allows the OTA transconductance to be tuned via the current biasing the gain-boosting circuit. The proposed transconductor has been designed in a 0.13- μm CMOS technology and powered from a 1.2-V supply. To show a possible application, a 0.5-MHz tunable third order Chebyshev low pass filter suitable for the Ultra Low Power Bluetooth Standard has been designed. The filter simulations show that all the requirements of the chosen standard are met, with good performance in terms of linearity, noise and power consumption.

Keywords—Transconductor; filter; body-driven; CMOS; Gm-C filters; gain boosting.

I. INTRODUCTION

High-performance Transconductors (OTAs) offering, as main features, linear and tunable transconductance with wide frequency bandwidth and high output resistance, are key elements in the design of continuous-time integrated filters adopting the Gm-C (or OTA-C) approach. Moreover, the increasing number of portable electronic applications requires circuits with ultra low-power capabilities. While frequency operation and power consumption issues are addressed by adopting low-threshold deep-submicron CMOS technologies, other performance parameters like linearity and output resistance result to be severely degraded by the same technologies [1]. More specifically, the maximum achievable small-signal output resistance of a nanometer MOS device is becoming unsatisfactorily low as it falls in the range of a few kilohms. Cascoding techniques are customarily adopted to face this problem, however standard cascoding approaches increase the supply voltage demand for a given output voltage swing to an extent that the advantage of using the advanced technology is almost lost.

To achieve very low voltage operation, approaches that exploit the MOS body terminal have been investigated until recently [2]-[9]. In this context, a gain boosted technique that exploits the body of the auxiliary gain-boosting transistor as input terminal was discussed in [10]. Compared to the standard low-voltage cascode approach, the body-driven one reduces the minimum supply requirement by two thresholds in a rail-to-rail structure adopting two complementary n-channel and p-channel sections [10].

In this paper, we will exploit this body-driven gain-boosting approach to design a low-voltage OTA whose tunability is achieved by varying the current that biases the gain boosting auxiliary circuit. No similar technique has been presented in the literature to the knowledge of the authors. The OTA is then used in the implementation of a third-order low-pass channel filter for a receiver based on the Ultra Low Power (ULP) Bluetooth Standard [11]. The OTA and the whole filter are designed in a 0.13- μm technology powered from a 1.2-V supply and simulated performances meet the stringent specifications of the chosen standard.

II. BODY-DRIVEN GAIN-BOOSTED OTA

The basic transconductor that will be used for reference in the design of the Bluetooth filter is depicted in Fig. 1a. It is a conventional pseudo differential cascode topology in which transistors M_1 (left and right side) are kept in their triode region (through V_{CN} and M_2). Transistors M_2 - M_4 are in saturation. Voltage V_{CTRL} is generated by the common-mode feedback circuit (described later) and sets the branch current through bias current generators M_4 . Unfortunately, this simple OTA provides an unacceptable low output resistance since it is dominated by $g_{m2}r_{d2}r_{d1}$, where r_{d1} is that of a triode-biased MOS. This low output resistance, coupled with the filter integrating capacitor, will severely impair the ultimate filter frequency performance.

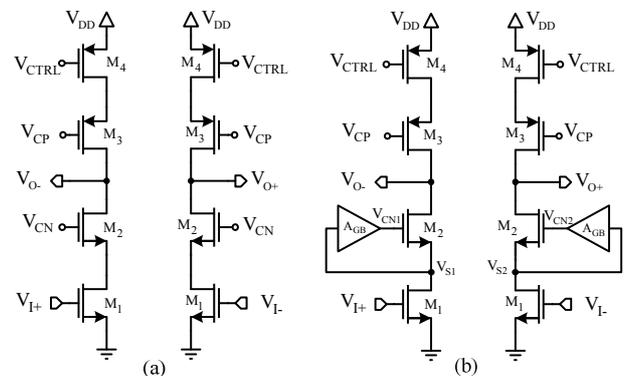


Figure 1. Basic pseudo-differential cascode OTA: (a) simple and (b) gain-boosted.

The gain-boosting technique [12] allows increasing the resistance seen at the drain of the cascode transistors. If A is the gain of the (inverting) auxiliary amplifier in Fig. 1b, then the output resistance of each terminal is given by

$$r_{out} = A g_{m2} r_{d2} r_{d1} // g_{m3} r_{d3} r_{d4} \quad (1)$$

Hence, the gain A allows compensating for the low value of r_{d1} . Besides, linearity is improved since the drain of the triode-biased transistor is kept to a nearly constant voltage irrespectively of the flowing current. At this purpose, the higher is the gain A , the higher is the linearity obtained. Of course, the bandwidth of the auxiliary amplifier must be higher than that of the main OTA for effective operation.

The auxiliary amplifier cannot be implemented with a conventional gate-driven transistor as it will unacceptably reduce the maximum signal swing. The body driven amplifier stage depicted in Fig. 2b can be profitably used to implement the auxiliary amplifiers in Fig. 1.

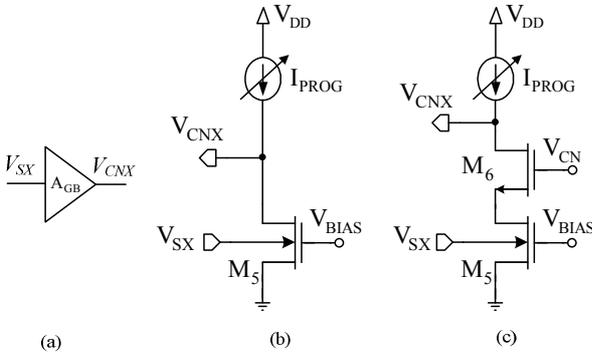


Figure 2. Body-driven amplifier stage (a) symbol (b) simple and (c) cascoded.

The gain of this stage is given by the source-bulk transconductance, g_{mb} , times the output resistance. Since the g_{mb} is lower than the gate transconductance, we can increase the dc gain by cascoding the body-driven transistor (and of course also the bias current generator, I_{PROG}). This option is illustrated in Fig. 2c. Both configurations need a bias voltages V_{BIAS} (V_{CN} is not critical) that is obtained through a suitable auxiliary circuit.

The complete scheme for the proposed pseudo-differential OTA is shown in Fig. 3. This figure includes the common-mode feedforward (CMFF) circuit required to set the common-mode current and a common-mode feedback circuit (CMFB) to fix the common-mode output voltage [13]-[14]. The CMFB circuit is composed of a branch similar to the CMFF circuit. The novel scheme proposed for the implementation of CMFF and CMFB performs a comparison of current to provide the control voltage V_{CTRL} . As node V_{CTRL} is a high impedance node, two Miller compensation capacitances are needed between node V_{CTRL} and the transconductor outputs (V_{o+} and V_{o-}) in order to guarantee the OTA stability. Moreover, the active load formed by transistors M_3 and M_4 is a voltage-controlled current source that ensures high output impedance.

Linear voltage to current conversion is achieved by applying the input voltage to the gate of the triode-operated transistor M_1 whereas remaining transistors are in saturation. Input transistors are kept in triode region by means of a regulated-cascode topology whose feedback loop is made by the body-driven amplifier stage.

The programmability of the OTA is performed by the drain to source voltage V_{DS1} . Voltage V_{DS1} can be set by two parameters of the gain boosting amplifier: the current generator I_{PROG} and the biasing voltage V_{BIAS} . In this design we have selected the first option as voltage V_{BIAS} should be kept controlled in order to avoid forward biasing of the bulk junction. V_{BIAS} can be obtained through replica-bias circuitry.

An approximate expression for the large-signal drain current of the input transistors operating in strong inversion and the ohmic region is given by:

$$I_{D1} = \beta_1 \left[(V_{GS1} - V_{TN}) \cdot V_{DS1} - \frac{V_{DS1}^2}{2} \right] \quad (2)$$

where $\beta_1 = \mu_n C_{ox} (W/L)_{M1}$ is the transconductance factor of transistor M_1 , and V_{GS1} is the input voltage V_i formed by the applied ac signal v_{in} superimposed to a common-mode voltage $V_{i,CM}$. The linear dependence of the transconductance on the control voltage is expressed as:

$$G_m = I_{out} / V_{id} = \beta_1 \cdot V_{DS1} \quad (3)$$

This ideal assumption is degraded in modern small geometry technologies. Large-channel input transistors have been used to reduce this effect.

III. CHANNEL FILTER OF THE ULP BLUETOOTH RECEIVER

The filter specifications for the channel filter of the receiver fulfilling the ULP Bluetooth Standard are summarized in the Table I.

TABLE I. FILTER SPECIFICATIONS FOR ULP BLUETOOTH

Specification	Value
Maximum peak to peak input voltage	140 mVpp
Maximum peak to peak output voltage	446 mVpp
Nominal current (under 1.2V supply)	250 μ A
Inband gain	10 dB
Cut-off frequency	500 kHz
Attenuation at 1 MHz (0 dB reference)	> 15 dB
Attenuation at 3 MHz (0 dB reference)	> 45 dB
Noise figure	< 40 dB
THD (in the bandpass and $V_{in} = 140mVpp$)	< -40 dB

In order to satisfy the attenuation constraints, a third order Chebyshev filter was found to be enough. The block diagram of the filter is shown in Fig. 4.

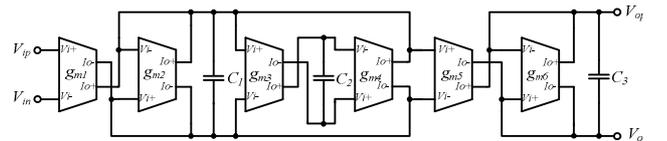


Figure 4. Third order Chebyshev low pass filter block diagram.

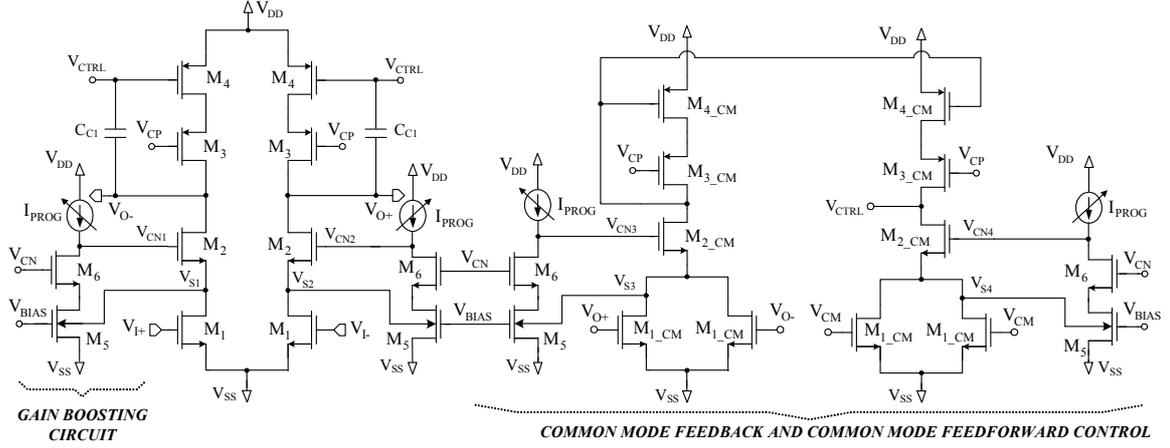


Figure 3. Complete scheme of the OTA.

The transfer function of this filter is reported in (4):

$$H(s) = \frac{2.219 \cdot 10^{19}}{s^3 + 3.936 \cdot 10^6 \cdot s^2 + 1.515 \cdot 10^{13} \cdot s + 2.219 \cdot 10^{19}} = \frac{1.128 \cdot 10^{13}}{s^2 + 1.968 \cdot 10^6 \cdot s + 1.128 \cdot 10^{13}} \cdot \frac{1.968 \cdot 10^6}{s + 1.968 \cdot 10^6} \quad (4)$$

IV. DESIGN AND SIMULATIONS

The OTA shown in Fig.1b, with the auxiliary amplifier in Fig. 2c and the bias and common-mode circuits (in Fig. 3) was designed in a 0.13- μm CMOS technology supplied with 1.2 V. Transistor dimensions are summarized in Table II. The compensation capacitances values are 300 fF.

TABLE II. OTA DESIGN SETTINGS AND TRANSISTOR DIMENSIONS

Transistor	W/L ($\mu\text{m}/\mu\text{m}$)
M ₁	0.8 μ /2.4 μ
M ₂	0.4 μ /0.4 μ
M ₃	9 μ /0.4 μ
M ₄	24 μ /1.6 μ
M ₅	8 μ /2.4 μ
M ₆	2 μ /0.26 μ
M _{1_CM}	0.2 μ /2.4 μ
M _{2_CM}	0.2 μ /0.4 μ
M _{3_CM}	4.5 μ /0.4 μ
M _{4_CM}	12 μ /1.6 μ

The linear performance of the OTA is given by the input voltage range for which the transconductance is constant. Fig. 5 illustrates the simulated dc transfer characteristic of the differential output current (I_{od}) versus the differential input voltage (V_{id}) for the tuning interval from $I_{PROG} = 18.5 \mu\text{A}$ to $20.5 \mu\text{A}$. For this tuning interval, the transconductance (dI_{od}/dV_{id}) ranges from $24 \mu\text{A/V}$ to $34 \mu\text{A/V}$. Note the good linearity obtained in the tuning range for an input voltage range from -300 mV to +300 mV.

The main OTA performance parameters are summarized in Table III, first column (OTA1). The other two columns report for comparison the performance of the gain-boosted OTA using the auxiliary amplifier circuit in Fig. 2b (OTA2) and of the simple non gain-boosted OTA in Fig. 1a (OTA3).

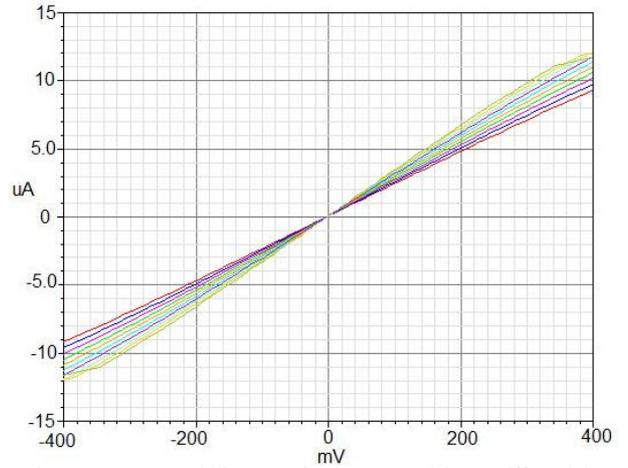


Figure 5. Programmability range for the proposed OTA. Differential output current versus differential input voltage.

TABLE III. MAIN OTA PERFORMANCE

Parameter	OTA1	OTA2	OTA3
G_m ($\mu\text{A/V}$)	27.4	26.9	22.2
R_{out} (M Ω)	54.4	15.2	1.84
THD @ 446mVpp, 100kHz	-52.7	-52.2	-44.5
NF @500kHz (dB)	42	42	39.8
Current consumption (μA)	101	103.5	24.7

It is apparent that the first OTA is superior in terms of output resistance and linearity obtained with a limited increase in current consumption. Hence, this OTA was used in the design of the filter. Table IV summarizes the transconductance and capacitor values adopted for the filter design.

TABLE IV. FILTER TRANSCONDUCTANCES AND CAPACITORS

Parameters	Values
G_{M1}	87 $\mu\text{A/V}$
G_{M2}	29 $\mu\text{A/V}$
G_{M3}	29 $\mu\text{A/V}$
G_{M4}	29 $\mu\text{A/V}$
G_{M5}	29 $\mu\text{A/V}$
G_{M6}	29 $\mu\text{A/V}$
C_1	7.37 pF
C_2	2.53 pF
C_3	7.37 pF

Fig. 6 illustrates the simulated frequency response at the nominal frequency of 0.5 MHz. The cut-off frequency can be tuned from 436 kHz to 594 kHz. A detail around the cut-off frequency has been also included. Fig. 7 shows the simulated output spectrum for a 100-kHz input signal of 140 mV peak-to-peak amplitude. We obtain -49 dB of THD for the nominal transconductance at $I_{PROG} = 19.1 \mu\text{A}$.

The main filter performance is summarized in Table V. ULP Bluetooth specifications are met.

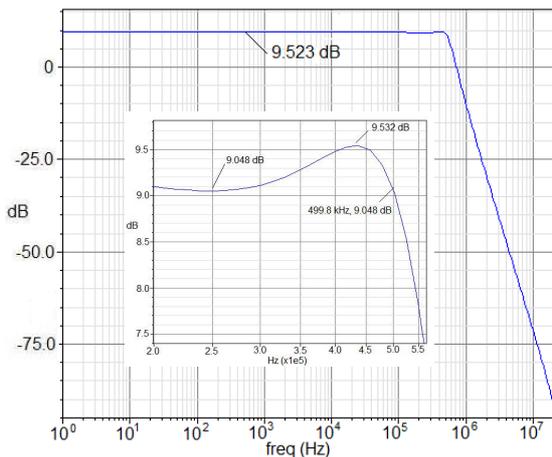


Fig. 6. Filter frequency response magnitude for an $I_{PROG} = 19.1 \mu\text{A}$

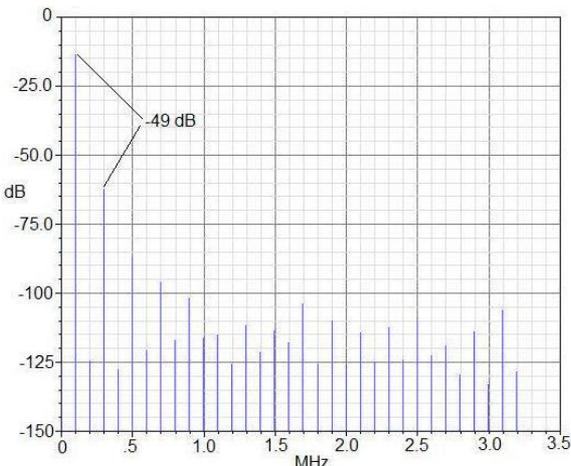


Fig. 7. Total harmonic distortion of the filter for a 70mVp input signal with a tone located at 100 kHz.

TABLE V. MAIN FILTER PERFORMANCE

Parameter	Value
Gain	9.52 – 9.5 dB
Cut-off frequency	436 – 594.3 kHz
Tunability current	18.3 – 20.5 μA
Noise figure	42 – 37 dB
V_{in}	140 mVpp
Maximum output voltage	414.8 – 418 mVpp
THD@100kHz for V_o	51.3 – 46.8 dB

V. CONCLUSIONS

We presented a gain boosting scheme based on bulk-driven transistors. This technique was exploited to design a linear pseudo-differential transconductor. The solution allows inherently to control the common-mode output voltage and provide tuning through the bias current of the boosting circuit. The properties of the transconductor make it very attractive for linear applications, especially at low supply voltages. In order to validate and find an application for the proposed 1.2-V 0.13- μm CMOS transconductor, a Gm-C filter for ULP Bluetooth applications has been implemented. Simulations results show that the filter meets the stringent standard specifications.

ACKNOWLEDGMENTS

This work has been supported by the Spanish Ministry of Science and Innovation under grants TEC2010-21563-C02-02 and FPA2010-22131-C02-02, by the Andalusian Innovation, Science and Enterprise Council, under grants TIC-2010-6583 and P09-TIC-4989 and by the Integrated Action Italy-Spain.

REFERENCES

- [1] C.J.-B. Fayomi, M. Sawan, and G.W. Roberts, "Reliable Circuit Techniques for Low-voltage Analog Design in Deep Submicron Standard CMOS: A Tutorial", *Analog Int. Circ. Sig. Proc.*, vol. 39, pp. 21-38, April 2004.
- [2] A. Guzinski, M. Bialko, and J. Matheau, "Body driven differential amplifier for application in continuous-time active-C filter," in *Proc. European Conf. Circ. Theory Design (ECCTD 1987)*, pp. 315-319, 1987.
- [3] R. Sarpeshkar, R. F. Lyon, and C. Mead, "A low-power wide-linear-range transconductance amplifier," *Analog Int. Circ. Sig. Proc.*, Vol. 13, pp. 123-151, May 1997.
- [4] B. J. Blalock, P. E. Allen, and G. A. Rincon-Mora, "Designing 1-V op amps using standard digital CMOS technology," *IEEE Trans. Circ. Syst. II*, Vol. 45, pp. 769-780, July 1998.
- [5] S. Chatterjee, Y. Tsvividis, and P. Kinget, "0.5-V analog circuit techniques and their application in OTA and filter design," *IEEE J. Solid-State Circ.*, Vol. 40, pp. 2373-2387, December 2005.
- [6] K.-P. Pun, S. Chatterjee, and P.R. Kinget, "A 0.5-V 74-dB SNDR 25-kHz continuous-time sigma-delta modulator with return-to-open DAC," *IEEE J. Solid-State Circ.*, Vol. 42, pp. 496-507, March 2007.
- [7] J. M. Carrillo, G. Torelli, R. Pérez-Aloe, and J. F. Duque-Carrillo, "1-V rail-to-rail CMOS opamp with improved bulk-driven input stage," *IEEE J. Solid-State Circ.*, Vol. 42, pp. 508-517, March 2007.
- [8] A.D. Grasso, P. Monsurrò, S. Pennisi, G. Scotti, A. Trifiletti, "Analysis and Implementation of a Minimum-Supply Body-Biased CMOS Differential Amplifier Cell," *IEEE Trans. VLSI*, Vol. 17, No. 2, pp. 172-180, Feb. 2009.
- [9] P. Monsurrò, S. Pennisi, G. Scotti and A. Trifiletti, "Linearization Technique for Source-Degenerated CMOS Differential Transconductors", *IEEE Trans. Circ. Syst.-II*, Vol. 54, No. 10, pp. 848-852, Oct. 2007.
- [10] P. Monsurrò, S. Pennisi, G. Scotti and A. Trifiletti, "0.9-V CMOS Cascode Amplifier with Body-driven Gain Boosting", *Int. J. Circ. Theory Appl.*, Vol. 37, No. 2, pp. 193-202, March 2009.
- [11] www.bluetooth.com/English/Products/Pages/Low_Energy.aspx.
- [12] K. Bult and G. Geelen, "The CMOS Gain-Boosting Technique", *Analog Integrated Circuits and Signal Processing*, Vol 1, pp. 119-135, 1991
- [13] J. Galán, M. Pedro, C. Rubia-Marcos, R.G. Carvajal, C. Luján-Martínez and A. López-Martín, "A Low-Voltage, High Linear Programmable Triode Transconductor", *Proceedings of 2010 IEEE International Symposium on Circuits and Systems (ISCAS)*, pp. 221-224, 2010.
- [14] A.N. Mohieldin, E. Sanchez-Sinencio and J. Silva-Martínez, "A fully balanced pseudo-differential OTA with common-mode feedforward and inherent common-mode feedback detector", *IEEE Journal of Solid-State Circuits*, Vol 38, pp. 663-668, 2003.